

Council for Technical Education and Vocational Training
Office of the Controller of Examinations
Sanothimi, Bhaktapur
Regular/Back Exam-2077, Chaitra

Program: Diploma in Computer Engineering Full Marks: 80
Year/Part: I/II (2018 New Course) Pass Marks: 32
Subject: Digital Logic Time: 3 hrs

*Candidates are required to give their answers in their own words as far as practicable.
The figures in the margin indicate full marks.*

Attempt Any Eight questions.

1. Differentiate between Analog and digital signal with example. Give brief description about types of number system. [5+5]
2. Perform the following : [5x2=10]
 - i) $(625.25)_{10} = (?)_2$
 - ii) $(12AB5)_{16} = (?)_2$
 - iii) $(1001101)_2 = (?)_8$
 - iv) $(111000)_2 = (?)_{10}$
 - v) $(101000110)_2 = (?)_{16}$
3. Define complement. Describe about 9's and 10's complement with suitable example. [2+4+4]
4. Explain briefly about basic gates. Prove that NOR gate as universal gate. [5+5]
5. State and prove De-Morgan's theorem. Differentiate between sum of product (SOP) and product of sum (POS). [5+5]
6. Define algebra with its laws. Simplify the following using K-map: [4+6]
 $F(A,B,C,D) = \sum (1,2,4,7,10,12,14) + \sum d (6,8,11,13)$
7. What do you mean by Decoder? Design a Decimal to BCD encoder with necessary logic diagram. [3+7]
8. Elaborate about JK and masters-slave flip-flop with its symbol and truth table. [5+5]
9. Compare between synchronous and Asynchronous counter. Explain about SIPO shift register in brief. [5+5]
10. Write short notes on : (Any Two) [2x5=10]
 - a) Gray Code
 - b) 4-to-1 multiplexer
 - c) Ring Counter
 - d) 7-segment display
 - e) Half Adder

Good Luck !

First Sem Digital Logic Fundamental Question Paper

Chandresh Agrawal, nandini books



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Fundamentals of Digital Logic with Verilog Design Stephen Brown,Zvonko Vranesic,2007-05-14 Fundamentals of Digital Logic With Verilog Design teaches the basic design techniques for logic circuits It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples Use of CAD software is well integrated into the book A CD ROM that contains Altera s Quartus CAD software comes free with every copy of the text The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays FPGAs and Complex Programmable Logic Devices CPLDs Students will be able to try firsthand the book s Verilog examples over 140 and homework problems Engineers use Quartus CAD for designing simulating testing and implementing logic circuits The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language Students will be able to enter a design into the CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices using the school s laboratory facilities Verilog is a complex language so it is introduced gradually in the book Each Verilog feature is presented as it becomes pertinent for the circuits being discussed To teach the student to use the Quartus CAD the book includes three tutorials [UPSC CSAT Prelims Exam 2022 Paper - 2 | 1000+ Solved Objective Questions \(10 Mock Tests + 3 Previous Year Papers\)](#) EduGorilla Prep Experts,2022-08-03 Best Selling Book in English Edition for UPSC

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SWITCHING THEORY AND LOGIC DESIGN A. ANAND KUMAR,2014-03-06 This comprehensive text on switching theory and logic design is designed for the undergraduate students of electronics and communication engineering electrical and electronics engineering electronics and instrumentation engineering telecommunication engineering computer science and engineering and information technology It will also be useful to AMIE IETE and diploma students Written in a student friendly style this book now in its Second Edition provides an in depth knowledge of switching theory and the design techniques of digital circuits Striking a balance between theory and practice it covers topics ranging from number systems binary codes logic gates and Boolean algebra to minimization using K maps and tabular method design of combinational logic circuits synchronous and asynchronous sequential circuits and algorithmic state machines The book discusses threshold gates and programmable logic devices PLDs In addition it elaborates on flip flops and shift registers Each chapter includes several fully worked out examples so that the students get a thorough grounding in related design concepts Short questions with answers review questions fill in the blanks multiple choice questions and problems are provided at the end of each chapter These help the students test their level of understanding of the subject and prepare for examinations confidently NEW TO THIS EDITION VHDL programs at the end of each chapter Complete answers with figures Several new problems with answers

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Digital Logic and Computer Design M. Morris Mano,2017 This book presents the basic concepts used in the design and analysis of digital systems and introduces the principles of digital computer organization and design

Fundamentals of Digital Logic with Verilog Design Stephen Brown,Zvonko

Vranesic,2013-03-15 Fundamentals of Digital Logic With Verilog Design teaches the basic design techniques for logic circuits It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples Use of CAD software is well integrated into the book A CD ROM that contains Altera s Quartus CAD software comes free with every copy of the text The CAD software provides automatic mapping of a design written in Verilog into Field Programmable Gate Arrays FPGAs and Complex Programmable Logic Devices CPLDs Students will be able to try firsthand the book s Verilog examples over 140 and homework problems Engineers use Quartus CAD for designing simulating testing and implementing logic circuits The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language Students will be able to enter a design into the CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices using the school s laboratory facilities Verilog is a complex language so it is introduced gradually in the book Each Verilog feature is presented as it becomes pertinent for the circuits being discussed To teach the student to use the Quartus CAD the book includes three tutorials

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