

[4136] - 203
MSc. (Sem. II)
ELECTRONIC SCIENCE
EL02UT06 : Digital System Design Using VHDL
(2008 Pattern)

Time : 3 Hours]

[Max. Marks : 80

Instructions to the candidates:-

- 1) All questions are compulsory.*
- 2) Figures to the right indicate full marks.*
- 3) Neat diagrams must be drawn wherever necessary.*

Q1) Attempt any two of the following.

[2 × 8 = 16]

- a) Explain in detail different VHDL object types. Explain predefined data types used in VHDL.
- b)
 - i) Write a VHDL code for J-K flip - flop.
 - ii) Write a function in VHDL for 4 - bit binary to gray code conversion.
- c) Explain design flow for digital system design using VHDL.

Q2) Attempt any four of the following.

[4 × 5 = 20]

- a) Design 2 - bit digital comparator which accepts two numbers $A = a_1a_0$ and $B = b_1b_0$ and gives outputs GT, EQ, LT.
- b) Implement the following function using 8 : 1 multiplexer.
 $F(A, B, C, D) = \sum m(0, 1, 3, 4, 8, 9, 15)$
- c) Design 3 - bit binary to gray code converter using 3:8 decoder.
- d) Write a VHDL code for 8:3 encoder using conditional signal assignment statement.
- e) Design 4 - bit parallel adder using look ahead carry generator.

Digital System Design Solved Question Papers

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