

CHAPTER 1 INTRODUCTION

- 1.1 An ADD/SUBTRACT logic circuit is shown below. It performs the ADD operation for $P = 0$, and SUBTRACT for $P = 1$.
- Draw an equivalent CMOS logic diagram by noting that most CMOS gates, except for transmission gate and XOR, are inverting. For example, AND gate is implemented with NAND followed by an inverter.
 - By using the gate array platform given on page 47, implement the CMOS circuit as compactly as possible with the aspect ratio, which is the ratio of vertical dimension to horizontal dimension, as close to 1 as possible.

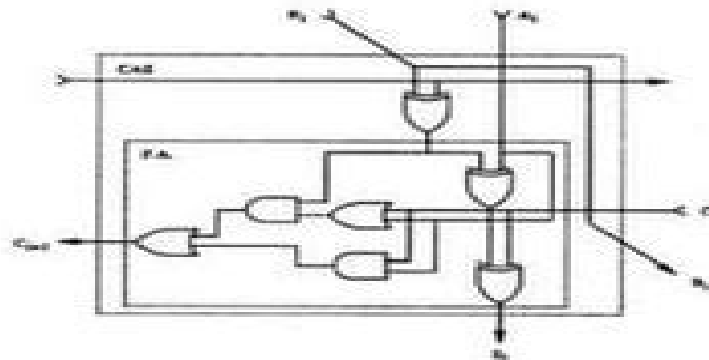


Figure P14.1(a)

SOLUTION:

- The CMOS circuit implementation using the gate array platform:

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Puja Mehta



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Digital Integrated Circuits DeMassa,1996-02-01 *Solutions Manual for Digital Integrated Circuits* Ayers John E,2003-09 **Solutions Manual to Accompany Analysis and Design of Digital Integrated Circuits** David A. Hodges,Yu Chen,Horace G. Jackson,1983 Solution Manual to Accompany CMOS Digital Integrated Circuits : Analysis and Design, Second Edition Sung-Mo Kang,Yusuf Leblebici,1999 **Solutions Manual Digital Integrated Circuits** CRC Press,2009-07-10 Any textbook more than five years old simply won't do in digital integrated circuits as dynamic CMOS circuits have emerged to dominate the field Providing a revised instructional text for engineers involved with Very Large Scale Integrated Circuit design and fabrication this second edition delves into the dramatic advances including new applications and changes in the physics of operation made possible by relentless miniaturization Each chapter includes numerous worked examples case studies and SPICE computer simulations The book's website offers supplementary material and more worked problems Qualifying instructors will have access to a new instructor's manual **CMOS Digital Integrated Circuits** Sung-Mo Kang,Yusuf Leblebici,2002 The fourth edition of CMOS Digital Integrated Circuits Analysis and Design continues the well established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design as well as addressing state of the art technology issues highlighted by the widespread use of nanometer scale CMOS technologies In this latest edition virtually all chapters have been re-written the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations and the material has been reinforced with up to date examples The broad ranging coverage of this textbook starts with the fundamentals of CMOS process technology and continues with MOS transistor models basic CMOS gates interconnect effects dynamic circuits memory circuits arithmetic building blocks clock and I/O circuits low power design techniques design for manufacturability and design for testability Solutions Manual for An Introduction to Digital and Analog Integrated Circuits and Applications Sanjit K. Mitra,Sanjit Kumar Mitra,1981 *Solutions Manual to Accompany Digital Concepts Using Standard Integrated Circuits* Richard S. Sandige,1978 **Solutions Manual** Kang,1995-08-01 **Solution Manual to Accompany Gallium Arsenide Digital Integrated Circuit Design** Stephen I. Long,Steven E. Butner,1991 **Device Electronics for Integrated Circuits** Richard S. Muller,Theodore I. Kamins,K. C. Hsieh,J. H. Kim,B. Y. Liu,1986 **Solutions Manual for an Introduction to Digital and Analog Integrated Circuits and Applications** Mitra,1986-01-01 Analog Integrated Circuit Design Tony Chan Carusone,David Johns,Kenneth William Martin,2012 The 2nd Edition of Analog Integrated Circuit Design focuses on more coverage about several types of circuits that have increased in importance in the past decade Furthermore the text is enhanced with material on CMOS IC device modeling updated processing layout and expanded coverage to reflect technical innovations CMOS devices and circuits have more influence in

this edition as well as a reduced amount of text on BiCMOS and bipolar information New chapters include topics on frequency response of analog ICs and basic theory of feedback amplifiers *Instructor's Manual for Digital Integrated Circuit Design* Ken Martin,2000 This manual is a gratis item to be given to instructors who have adopted Digital Integrated Circuit Design by Ken Martin This manual contains complete solutions prepared by the author to all of the exercises in the text **Solutions Manual to Accompany Gallium Arsenide Digital Integrated Circuit Design** Stephen I. Long,Steven E. Butner,1991 **Solutions Manual to Accompany Integrated Electronics** Jacob Millman,Christos C Halkias,George A. Katopis,1972 **Solution Manual** James W. Mayer,S. S. Lau,1990 **Electronic design with integrated circuits** David J. Comer,1981-01-01 **Electronic Devices and Circuit Fundamentals, Solution Manual** Dale R Patrick,Stephen W. Fardo,Ray E. Richardson,Vigyan (Vigs) Chandra,2023-05-26 Devices and Circuit Fundamentals is Chapter Outline Learning Objectives Key Terms Figure List Chapter Summary Formulas Answers to Examples Self Exams Glossary of Terms defined

CMOS(Complementary Metal Oxide Semiconductor) — MOS (Metal Oxide Semiconductor) Behzad Razavi,2005 CMOS MOS

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