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10EC664

**Sixth Semester B.E. Degree Examination, June/July 2017**  
**Low Power VLSI Design**

Time: 3 hrs.

Max. Marks: 100

*Note: Answer FIVE full questions, selecting  
at least TWO questions from each part.*

**PART – A**

- 1 a. What are the different sources of power dissipation? (04 Marks)  
b. Explain the energy band diagrams of MIS-structure at different bias conditions. (12 Marks)  
c. Explain briefly long channel MOSFET. (04 Marks)
- 2 a. Derive and show that expression for short circuit dissipation is given by,  
$$P_{sc} = \frac{\beta}{12} (V_{DD} - V_T)^2 \frac{\tau}{T}$$
 (12 Marks)  
b. Explain the charge sharing phenomenon in dynamic circuits. (08 Marks)
- 3 a. Explain the conceptual level diagrams of possible improvement in power dissipation at various levels. (08 Marks)  
b. Derive the first order difference algorithm for a discrete LTI-FIR system. (06 Marks)  
c. Explain the data flow graph of FIR filter. (06 Marks)
- 4 a. Explain the power optimization using operation reduction techniques. (06 Marks)  
b. Explain FSM and combinational logic synthesis with suitable state machine representation. (06 Marks)  
c. Using Shannon's expansion principle, explain the precomputation of adder-comparator circuit. (08 Marks)

**PART – B**

- 5 a. Explain the ratio-logic using only nMOS and pseudo nMOS logic network. (08 Marks)  
b. Realize and implement NAND/AND logic and XOR/XNOR logic using CPL logic. (06 Marks)  
c. What are the factors influencing leakage current in deep submicrometer transistor? (06 Marks)
- 6 a. Explain the low voltage design techniques with respect to reverse  $V_{gs}$ , steeper sub threshold swing and multiple thresholds. (10 Marks)  
b. Explain the importance of multiple supply voltages and multiple threshold techniques. (10 Marks)
- 7 a. Design NAND gate using adiabatic switching for reversible logic. (10 Marks)  
b. Explain energy recovery SRAM-core organization. (06 Marks)  
c. Explain briefly supply clock generation technique. (04 Marks)
- 8 a. Explain various factors essential for software power estimation. (10 Marks)  
b. Explain the software power estimation with suitable example. (10 Marks)

\* \* \* \* \*

# **Vlsi Design Sixth Sem Question Paper**

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# Vlsi Design Sixth Sem Question Paper

Principles of VLSI and CMOS Integrated Circuits Jain Richa & Rai Amrita, For B E B Tech students of all Technical Universities Microelectronics VLSI Design is an emerging subject in the field of electronics in recent years It is an introductory source to internal parts of electronics at minute level This book is covering CMOS Design from a digital system level to circuit level and providing a background in CMOS Processing Technology The book includes basic theoretical knowledge as well as good engineering practice This book is recommended for B Tech M Tech and diploma students of all Indian Universities and also useful for competitive examinations

Proceedings of ASP-DAC/VLSI Design 2002 ,2002 Papers from a January 2002 conference are organized into four sessions each on low power design synthesis testing layout and interconnects and technology as well as two sessions each on embedded systems verification and VLSI architecture one session on analog design and one session on hot c

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Publications of the National Institute of Standards and Technology ... Catalog National Institute of Standards and Technology (U.S.),1991

**Digital VLSI Systems Design** Seetharaman Ramachandran,2007-06-14 This book provides step by step guidance on how to design VLSI systems using Verilog It shows the way to design systems that are device vendor and technology independent Coverage presents new material and theory as well as synthesis of recent work with complete Project Designs using industry standard CAD tools and FPGA boards The reader is taken step by step through different designs from implementing a single digital gate to a massive design consuming well over 100 000 gates All the design codes developed in this book are Register Transfer Level RTL compliant and can be readily used or amended to suit new projects

**Digital Integrated Circuit Design** Hubert Kaeslin,2008-04-28 This practical tool independent guide to designing digital circuits takes a unique top down approach reflecting the nature of the design process in industry Starting with architecture design the book comprehensively explains the why and how of digital circuit design using the physics designers need to know and no more

**Electrical & Electronics Abstracts** ,1997

**Proceedings of the ASP-DAC ... Asia and South Pacific Design Automation Conference** ,2002 *Scientific and Technical Aerospace Reports* ,1987

**Structural Mechanics: Modelling and Analysis of Frames and Trusses** Karl-Gunnar Olsson,Ola Dahlblom,2016-01-26 Textbook covers the fundamental theory of structural mechanics and the modelling and analysis of frame and truss structures Deals with modelling and analysis of trusses and frames using a systematic matrix formulated displacement method with the language and flexibility of the finite element method Element matrices are established from analytical solutions to the differential equations Provides a strong toolbox with elements and algorithms for computational modelling and numerical exploration of truss and frame structures Discusses the concept of

stiffness as a qualitative tool to explain structural behaviour Includes numerous exercises for some of which the computer software CALFEM is used In order to support the learning process CALFEM gives the user full overview of the matrices and algorithms used in a finite element analysis

**Index to IEEE Publications** Institute of Electrical and Electronics Engineers,1996 Issues for 1973 cover the entire IEEE technical literature

**Problems in Elementary Physics** Arihant Experts,2019-01-11

**CMOS** (————) Behzad Razavi,2005 CMOS MOS **CMOS** R. Jacob Baker,2008 This edition provides an important contemporary view of a wide range of analog digital circuit blocks the BSIM model data converter architectures and more The authors develop design techniques for both long and short channel CMOS technologies and then compare the two

**Fundamentals of Modern VLSI Devices** Yuan Taur,Tak H. Ning,2013-05-02 Learn the basic properties and designs of modern VLSI devices as well as the factors affecting performance with this thoroughly updated second edition The first edition has been widely adopted as a standard textbook in microelectronics in many major US universities and worldwide The internationally renowned authors highlight the intricate interdependencies and subtle trade offs between various practically important device parameters and provide an in depth discussion of device scaling and scaling limits of CMOS and bipolar devices Equations and parameters provided are checked continuously against the reality of silicon data making the book equally useful in practical transistor design and in the classroom Every chapter has been updated to include the latest developments such as MOSFET scale length theory high field transport model and SiGe base bipolar devices

**Proceedings of MELECON** ,1998 **IJCAI-89** N. S. Sridharan,1989 *The Bent of Tau Beta Pi* ,1991

**Digital Electronics** Anil K. Maini,2007-09-27 The fundamentals and implementation of digital electronics are essential to understanding the design and working of consumer industrial electronics communications embedded systems computers security and military equipment Devices used in applications such as these are constantly decreasing in size and employing more complex technology It is therefore essential for engineers and students to understand the fundamentals implementation and application principles of digital electronics devices and integrated circuits This is so that they can use the most appropriate and effective technique to suit their technical need This book provides practical and comprehensive coverage of digital electronics bringing together information on fundamental theory operational aspects and potential applications With worked problems examples and review questions for each chapter Digital Electronics includes information on number systems binary codes digital arithmetic logic gates and families and Boolean algebra an in depth look at multiplexers de multiplexers devices for arithmetic operations flip flops and related devices counters and registers and data conversion circuits up to date coverage of recent application fields such as programmable logic devices microprocessors microcontrollers digital troubleshooting and digital instrumentation A comprehensive must read book on digital electronics for senior undergraduate and graduate students of electrical electronics and computer engineering and a valuable reference book for professionals and researchers

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### **Table of Contents Vlsi Design Sixth Sem Question Paper**

1. Understanding the eBook Vlsi Design Sixth Sem Question Paper
  - The Rise of Digital Reading Vlsi Design Sixth Sem Question Paper
  - Advantages of eBooks Over Traditional Books
2. Identifying Vlsi Design Sixth Sem Question Paper
  - Exploring Different Genres
  - Considering Fiction vs. Non-Fiction
  - Determining Your Reading Goals

3. Choosing the Right eBook Platform
  - Popular eBook Platforms
  - Features to Look for in an Vlsi Design Sixth Sem Question Paper
  - User-Friendly Interface
4. Exploring eBook Recommendations from Vlsi Design Sixth Sem Question Paper
  - Personalized Recommendations
  - Vlsi Design Sixth Sem Question Paper User Reviews and Ratings
  - Vlsi Design Sixth Sem Question Paper and Bestseller Lists
5. Accessing Vlsi Design Sixth Sem Question Paper Free and Paid eBooks
  - Vlsi Design Sixth Sem Question Paper Public Domain eBooks
  - Vlsi Design Sixth Sem Question Paper eBook Subscription Services
  - Vlsi Design Sixth Sem Question Paper Budget-Friendly Options
6. Navigating Vlsi Design Sixth Sem Question Paper eBook Formats
  - ePub, PDF, MOBI, and More
  - Vlsi Design Sixth Sem Question Paper Compatibility with Devices
  - Vlsi Design Sixth Sem Question Paper Enhanced eBook Features
7. Enhancing Your Reading Experience
  - Adjustable Fonts and Text Sizes of Vlsi Design Sixth Sem Question Paper
  - Highlighting and Note-Taking Vlsi Design Sixth Sem Question Paper
  - Interactive Elements Vlsi Design Sixth Sem Question Paper
8. Staying Engaged with Vlsi Design Sixth Sem Question Paper
  - Joining Online Reading Communities
  - Participating in Virtual Book Clubs
  - Following Authors and Publishers Vlsi Design Sixth Sem Question Paper
9. Balancing eBooks and Physical Books Vlsi Design Sixth Sem Question Paper
  - Benefits of a Digital Library
  - Creating a Diverse Reading Collection Vlsi Design Sixth Sem Question Paper
10. Overcoming Reading Challenges
  - Dealing with Digital Eye Strain
  - Minimizing Distractions

- Managing Screen Time
- 11. Cultivating a Reading Routine Vlsi Design Sixth Sem Question Paper
  - Setting Reading Goals Vlsi Design Sixth Sem Question Paper
  - Carving Out Dedicated Reading Time
- 12. Sourcing Reliable Information of Vlsi Design Sixth Sem Question Paper
  - Fact-Checking eBook Content of Vlsi Design Sixth Sem Question Paper
  - Distinguishing Credible Sources
- 13. Promoting Lifelong Learning
  - Utilizing eBooks for Skill Development
  - Exploring Educational eBooks
- 14. Embracing eBook Trends
  - Integration of Multimedia Elements
  - Interactive and Gamified eBooks

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