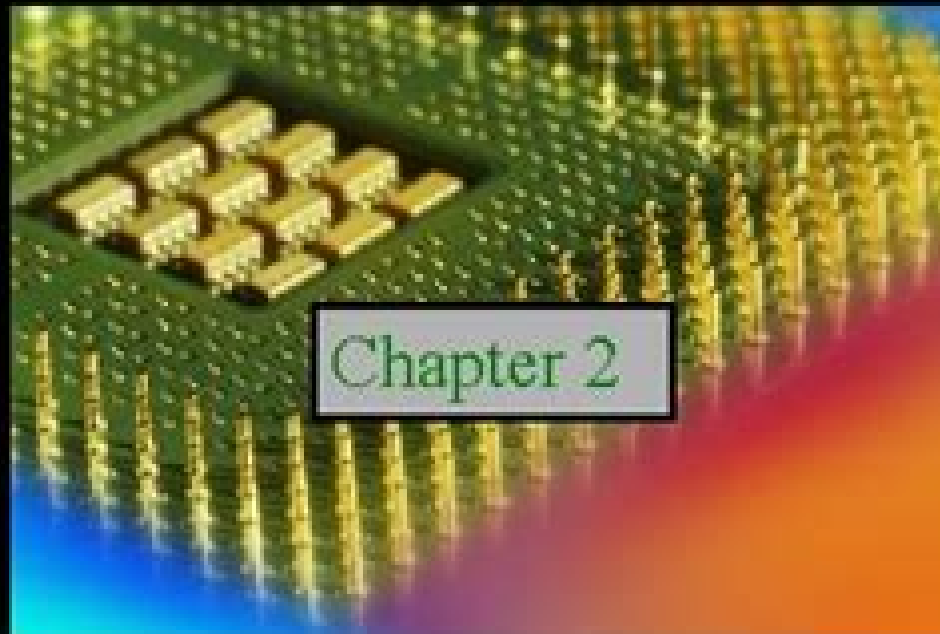


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Tenth Edition

Floyd



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**Charles K. Alexander, Matthew N. O.  
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concepts hands on applications and troubleshooting Written in a clear and accessible narrative the Seventh Edition focuses on fundamental principles and their applications to solving real circuit analysis problems and devotes six chapters to examining electronic devices **FUNDAMENTALS OF DIGITAL CIRCUITS, Fourth Edition** KUMAR, A.

ANAND,2016-07-18 The Fourth edition of this well received text continues to provide coherent and comprehensive coverage of digital circuits It is designed for the undergraduate students pursuing courses in areas of engineering disciplines such as Electrical and Electronics Electronics and Communication Electronics and Instrumentation Telecommunications Medical Electronics Computer Science and Engineering Electronics and Computers and Information Technology It is also useful as a text for MCA M Sc Electronics and M Sc Computer Science students Appropriate for self study the book is useful even for AMIE and grad IETE students Written in a student friendly style the book provides an excellent introduction to digital concepts and basic design techniques of digital circuits It discusses Boolean algebra concepts and their application to digital circuitry and elaborates on both combinational and sequential circuits It provides numerous fully worked out laboratory tested examples to give students a solid grounding in the related design concepts It includes a number of short questions with answers review questions fill in the blanks with answers multiple choice questions with answers and exercise problems at the end of each chapter As the book requires only an elementary knowledge of electronics to understand most of the topics it can also serve as a textbook for the students of polytechnics B Sc Electronics and B Sc Computer Science **NEW TO THIS EDITION** Now based on the readers demand this new edition incorporates VERILOG programs in addition to VHDL programs at the end of each chapter *Experiments in Basic Circuits* David M. Buchla,2007 *Digital Logic and Computer Design*

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Arrays FPGAs and Complex Programmable Logic Devices CPLDs Students will be able to try firsthand the book s Verilog examples over 140 and homework problems Engineers use Quartus CAD for designing simulating testing and implementing logic circuits The version included with this text supports all major features of the commercial product and comes with a compiler for the IEEE standard Verilog language Students will be able to enter a design into the CAD system compile the design into a selected device simulate the functionality and timing of the resulting circuit implement the designs in actual devices using the school s laboratory facilities Verilog is a complex language so it is introduced gradually in the book Each Verilog feature is presented as it becomes pertinent for the circuits being discussed To teach the student to use the Quartus CAD the book includes three tutorials

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