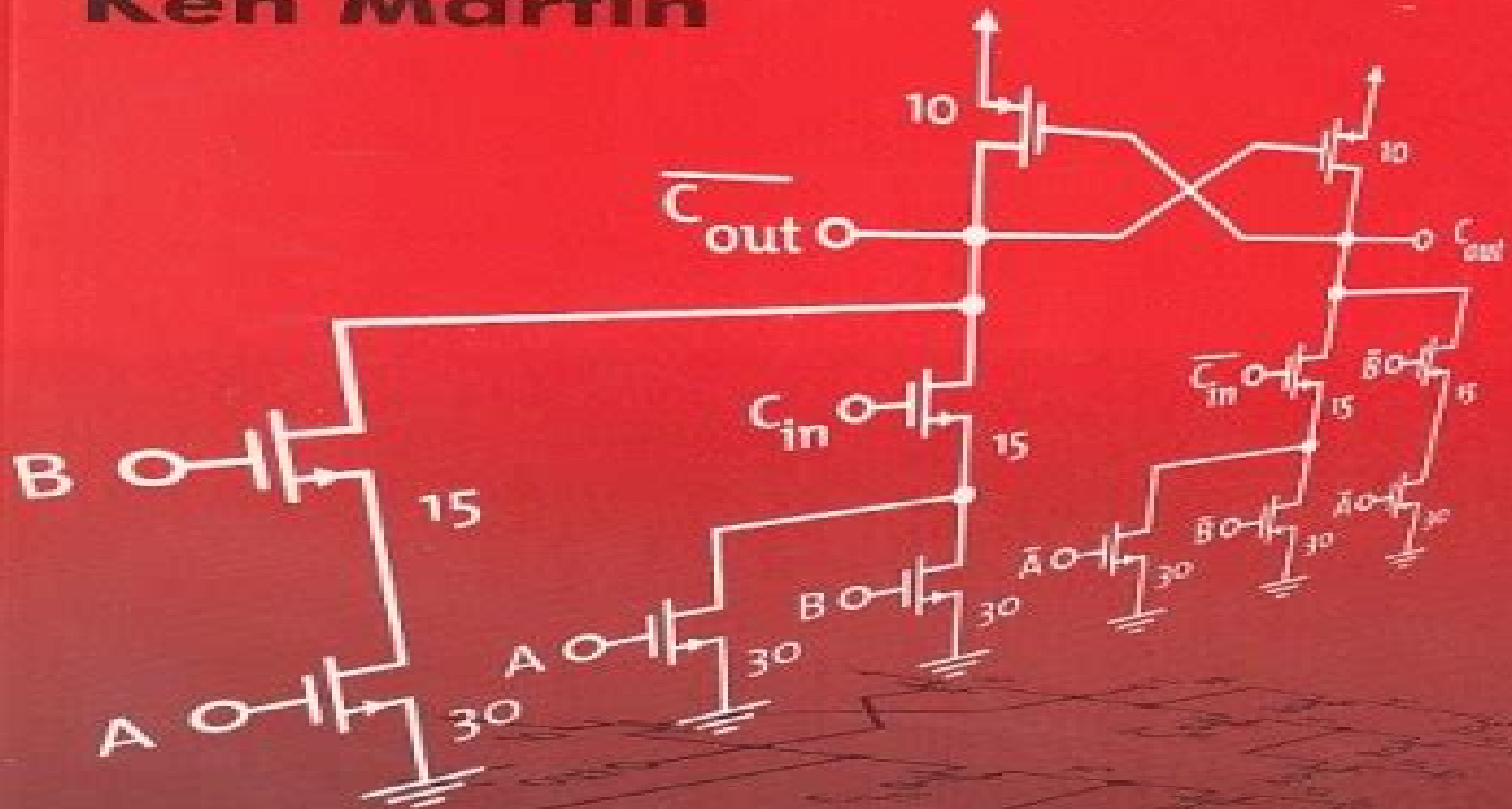


Digital Integrated Circuit Design

Ken Martin



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Jan M. Rabaey



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Analog Integrated Circuit Design Tony Chan Carusone, David Johns, Kenneth Martin, 2011-12-13 When first published in 1996 this text by David Johns and Kenneth Martin quickly became a leading textbook for the advanced course on Analog IC Design This new edition has been thoroughly revised and updated by Tony Chan Carusone a University of Toronto colleague of Drs Johns and Martin Dr Chan Carusone is a specialist in analog and digital IC design in communications and signal processing This edition features extensive new material on CMOS IC device modeling processing and layout Coverage has been added on several types of circuits that have increased in importance in the past decade such as generalized integer N phase locked loops and their phase noise analysis voltage regulators and 1.5b per stage pipelined A/D converters Two new chapters have been added to make the book more accessible to beginners in the field frequency response of analog ICs and basic theory of feedback amplifiers

Digital Integrated Circuits Jan M. Rabaey, 1996 Beginning with discussions on the operation of electronic devices and analysis of the nucleus of digital design the text addresses the impact of interconnect design for low power issues in timing and clocking design methodologies and the effect of design automation on the digital design perspective

Digital Integrated Circuit Design Kenneth William Martin, 2000 The impact of digital integrated circuits on our modern society has been pervasive They are the enabling technology of the current computer and information technology revolution This is largely true because of the immense amount of signal and computer processing that can be realized in a single integrated circuit modern ICs may contain millions of logic gates This text book is intended to take a reader having only a minimal background and knowledge in electronics to the point where they can design state of the art digital integrated circuits Designing high performance digital integrated circuits requires expertise in many different areas These include semiconductor physics integrated circuit processing transistor level design logic level design system level design testing etc Aspects of these topics are covered throughout this text although the emphasis is on transistor level design of digital integrated circuits and systems This is in contrast to the perspective in many other texts which takes a system level or VLSI approach where transistor level details are minimized It is the author's belief that before system level considerations can be properly evaluated an in depth transistor level understanding must first be obtained Important system level considerations such as timing pipe lining clock distribution and system building blocks are covered in detail but the emphasis on transistors first Throughout the book physical and intuitive explanations are given and although mathematical quantitative analysis of many circuits have necessarily been presented Martin has attempted not to miss seeing the forest because of the trees This book presents the critical underlying concepts without becoming entangled in tedious and over complicated circuit analyses It is intended for senior graduate level students in electrical and computer engineering This course assumes the Sedra Smith Microelectronic Circuits course as a prerequisite

Computer-Aided Design of Analog Integrated Circuits

and Systems Rob A. Rutenbar, Georges G. E. Gielen, 2002-05-06 The tools and techniques you need to break the analog design bottleneck Ten years ago analog seemed to be a dead end technology Today System on Chip SoC designs are increasingly mixed signal designs With the advent of application specific integrated circuits ASIC technologies that can integrate both analog and digital functions on a single chip analog has become more crucial than ever to the design process Today designers are moving beyond hand crafted one transistor at a time methods They are using new circuit and physical synthesis tools to design practical analog circuits new modeling and analysis tools to allow rapid exploration of system level alternatives and new simulation tools to provide accurate answers for analog circuit behaviors and interactions that were considered impossible to handle only a few years ago To give circuit designers and CAD professionals a better understanding of the history and the current state of the art in the field this volume collects in one place the essential set of analog CAD papers that form the foundation of today s new analog design automation tools Areas covered are Analog synthesis Symbolic analysis Analog layout Analog modeling and analysis Specialized analog simulation Circuit centering and yield optimization Circuit testing Computer Aided Design of Analog Integrated Circuits and Systems is the cutting edge reference that will be an invaluable resource for every semiconductor circuit designer and CAD professional who hopes to break the analog design bottleneck

Digital Integrated Circuits Thomas A. DeMassa, Zack Ciccone, 1996 Contains the most extensive coverage of digital integrated circuits available in a single source Provides complete qualitative descriptions of circuit operation followed by in depth analytical analyses and spice simulations The circuit families described in detail are transistor transistor logic TTL STTL and ASTTL emitter coupled logic ECL NMOS logic CMOS logic dynamic CMOS BiCMOS structures and various GASFET technologies In addition to detailed presentation of the basic inverter circuits for each digital logic family complete details of other logic circuits for these families are presented

Principles of Asynchronous Circuit Design Jens Sparsø, Steve Furber, 2013-04-17 Principles of Asynchronous Circuit Design A Systems Perspective addresses the need for an introductory text on asynchronous circuit design Part I is an 8 chapter tutorial which addresses the most important issues for the beginner including how to think about asynchronous systems Part II is a 4 chapter introduction to Balsa a freely available synthesis system for asynchronous circuits which will enable the reader to get hands on experience of designing high level asynchronous systems Part III offers a number of examples of state of the art asynchronous systems to illustrate what can be built using asynchronous techniques The examples range from a complete commercial smart card chip to complex microprocessors The objective in writing this book has been to enable industrial designers with a background in conventional clocked design to be able to understand asynchronous design sufficiently to assess what it has to offer and whether it might be advantageous in their next design task

Digital Design Mohammad A. Karim, Xinghao Chen, 2017-12-19 In today s digital design environment engineers must achieve quick turn around time with ready accesses to circuit synthesis and simulation applications This type of productivity relies on the principles and practices of computer aided design CAD Digital

Design Basic Concepts and Principles addresses the many challenging issues critical to today's digital design practices such as hazards and logic minimization finite state machine synthesis cycles and races and testability theories while providing hands on experience using one of the industry's most popular design application Xilinx Web PACK™ The authors begin by discussing conventional and unconventional number systems binary coding theories and arithmetic as well as logic functions and Boolean algebra Building upon classic theories of digital systems the book illustrates the importance of logic minimization using the Karnaugh map technique It continues by discussing implementation options and examining the pros and cons of each method in addition to an assessment of tradeoffs that often accompany design practices The book also covers testability emphasizing that a good digital design must be easy to verify and test with the lowest cost possible Throughout the text the authors analyze combinational and sequential logic elements and illustrate the designs of these components in structural hierarchical and behavior VHDL descriptions Covering fundamentals and best practices Digital Design Basic Concepts and Principles provides you with critical knowledge of how each digital component ties together to form a system and develops the skills you need to design and simulate these digital components using modern CAD software

CMOS Digital Integrated Circuits Sung-Mo Kang, Yusuf Leblebici, 2002 The fourth edition of CMOS Digital Integrated Circuits Analysis and Design continues the well established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design as well as addressing state of the art technology issues highlighted by the widespread use of nanometer scale CMOS technologies In this latest edition virtually all chapters have been re written the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations and the material has been reinforced with up to date examples The broad ranging coverage of this textbook starts with the fundamentals of CMOS process technology and continues with MOS transistor models basic CMOS gates interconnect effects dynamic circuits memory circuits arithmetic building blocks clock and I O circuits low power design techniques design for manufacturability and design for testability Analysis and Design of Digital Integrated Circuits David A. Hodges, Horace G. Jackson, Resve A. Saleh, 2003 The third edition of Hodges and Jackson's Analysis and Design of Digital Integrated Circuits has been thoroughly revised and updated by a new co author Resve Saleh of the University of British Columbia The new edition combines the approachability and concise nature of the Hodges and Jackson classic with a complete overhaul to bring the book into the 21st century The new edition has replaced the emphasis on BiPolar with an emphasis on CMOS The outdated MOS transistor model used throughout the book will be replaced with the now standard deep submicron model The material on memory has been expanded and updated As well the book now includes more on SPICE simulation and new problems that reflect recent technologies The emphasis of the book is on design but it does not neglect analysis and has as a goal to provide enough information so that a student can carry out analysis as well as be able to design a circuit This book provides an excellent and balanced introduction to digital circuit

design for both students and professionals **Designing Analog Chips** Hans Camenzind,2005 A comprehensive introduction to CMOS and bipolar analog IC design The book presumes no prior knowledge of linear design making it comprehensible to engineers with a non analog back ground The emphasis is on practical design covering the entire field with hundreds of examples to explain the choices Concepts are presented following the history of their discovery Content 1 Devices Semiconductors The Bipolar Transistor The Integrated Circuit Integrated NPN Transistors The Case of the Lateral PNP Transistor CMOS Transistors The Substrate PNP Transistor Diodes Zener Diodes Resistors Capacitors CMOS vs Bipolar 2 Simulation DC Analysis AC Analysis Transient Analysis Variations Models Diode Model Bipolar Transistor Model Model for the Lateral PNP Transistor MOS Transistor Models Resistor Models Models for Capacitors 3 Current Mirrors 4 Differential Pairs 5 Current Sources 6 Time Out Analog Measures dB RMS Noise Fourier Analysis Distortion Frequency Compensation 7 Bandgap References 8 Op Amps 9 Comparators 10 Transimpedance Amplifiers 11 Timers and Oscillators 12 Phase Locked Loops 13 Filters 14 Power Linear Regulators Low Drop Out Regulators Switching Regulators Linear Power Amplifiers Switching Power Amplifiers 15 A to D and D to A The Delta Sigma Converter 16 Odds and Ends Gilbert Cell Multipliers Peak Detectors Rectifiers and Averaging Circuits Thermometers Zero Crossing Detectors 17 Layout Charge-Based MOS Transistor Modeling Christian C. Enz, Eric A. Vittoz, 2006-08-14 Modern large scale analog integrated circuits ICs are essentially composed of metal oxide semiconductor MOS transistors and their interconnections As technology scales down to deep sub micron dimensions and supply voltage decreases to reduce power consumption these complex analog circuits are even more dependent on the exact behavior of each transistor High performance analog circuit design requires a very detailed model of the transistor describing accurately its static and dynamic behaviors its noise and matching limitations and its temperature variations The charge based EKV Enz Krummenacher Vittoz MOS transistor model for IC design has been developed to provide a clear understanding of the device properties without the use of complicated equations All the static dynamic noise non quasi static models are completely described in terms of the inversion charge at the source and at the drain taking advantage of the symmetry of the device Thanks to its hierarchical structure the model offers several coherent description levels from basic hand calculation equations to complete computer simulation model It is also compact with a minimum number of process dependant device parameters Written by its developers this book provides a comprehensive treatment of the EKV charge based model of the MOS transistor for the design and simulation of low power analog and RF ICs Clearly split into three parts the authors systematically examine the basic long channel intrinsic charge based model including all the fundamental aspects of the EKV MOST model such as the basic large signal static model the noise model and a discussion of temperature effects and matching properties the extended charge based model presenting important information for understanding the operation of deep submicron devices the high frequency model setting out a complete MOS transistor model required for designing RF CMOS integrated circuits Practising engineers and circuit designers in the

semiconductor device and electronics systems industry will find this book a valuable guide to the modelling of MOS transistors for integrated circuits It is also a useful reference for advanced students in electrical and computer engineering

High-Frequency Integrated Circuits Sorin Voinigescu,2013-02-28 A transistor level design intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband systems from 2 GHz to 200 GHz this comprehensive text covers high speed RF mm wave and optical fibre circuits using nanoscale CMOS SiGe BiCMOS and III V technologies Step by step design methodologies end of chapter problems and practical simulation and design projects are provided making this an ideal resource for senior undergraduate and graduate courses in circuit design With an emphasis on device circuit topology interaction and optimization it gives circuit designers and students alike an in depth understanding of device structures and process limitations affecting circuit performance

ANALOG MOS INTEGRATED CIRCUITS FOR SIGNAL PROCESSING Roubik Gregorian,Gabor C. Temes,2008-09-01 Market_Desc Engineers Managers Technicians About The Book The book describes the operating principles of analog MOS integrated circuits and how to design and use such circuits The initial section explores general properties of analog MOS integrated circuits and the math and physics background required The remainder of the book is devoted to the design of circuits It includes such devices as switched capacitor filters analog to digital and digital to analog converters amplifiers modulators oscillators and others Tables and numerical design examples clarify the step by step processes involved An Instructor s Manual presenting detailed solutions to all the problems in the book is available from the Wiley editorial department

CMOS analog circuit design Allen Philip & Holberg Doug,Phillip E. Allen,Douglas R. Holberg,2010 CMOS Analog Integrated Circuits Tertulien Ndjountche,2019-12-17 High speed power efficient analog integrated circuits can be used as standalone devices or to interface modern digital signal processors and micro controllers in various applications including multimedia communication instrumentation and control systems New architectures and low device geometry of complementary metaloxide semiconductor CMOS technologies have accelerated the movement toward system on a chip design which merges analog circuits with digital and radio frequency components

The Design of CMOS Radio-Frequency Integrated Circuits Thomas H. Lee,2004 This book first published in 2004 is an expanded and revised edition of Tom Lee s acclaimed RFIC text *Radio Frequency Integrated Circuit Design* John W. M. Rogers,Calvin Plett,2010 This newly revised and expanded edition of the 2003 Artech House classic Radio Frequency Integrated Circuit Design serves as an up to date practical reference for complete RFIC know how The second edition includes numerous updates including greater coverage of CMOS PA design RFIC design with on chip components and more worked examples with simulation results By emphasizing working designs this book practically transports you into the authors own RFIC lab so you can fully understand the function of each design detailed in this book Among the RFIC designs examined are RF integrated LC based filters VCO automatic amplitude control loops and fully integrated transformer based circuits as well as image reject mixers and power

amplifiers If you are new to RFIC design you can benefit from the introduction to basic theory so you can quickly come up to speed on how RFICs perform and work together in a communications device A thorough examination of RFIC technology guides you in knowing when RFICs are the right choice for designing a communication device This leading edge resource is packed with over 1 000 equations and more than 435 illustrations that support key topics **Troubleshooting Analog Circuits** Robert Pease,1991-06-19 Whether you are primarily an analog or digital engineer technician experienced or neophyte this book has something for you You ll find Bob s approach to problem identification and isolation to be applicable to a wide spectrum of engineering disciplines **Computer Organization and Design RISC-V Edition** David A. Patterson,John L. Hennessy,2017-05-12 The new RISC V Edition of Computer Organization and Design features the RISC V open source instruction set architecture the first open source architecture designed to be used in modern computing environments such as cloud computing mobile devices and other embedded systems With the post PC era now upon us Computer Organization and Design moves forward to explore this generational change with examples exercises and material highlighting the emergence of mobile computing and the Cloud Updated content featuring tablet computers Cloud infrastructure and the x86 cloud computing and ARM mobile computing devices architectures is included An online companion Web site provides advanced content for further study appendices glossary references and recommended reading Features RISC V the first such architecture designed to be used in modern computing environments such as cloud computing mobile devices and other embedded systems Includes relevant examples exercises and material highlighting the emergence of mobile computing and the cloud **CMOS** Behzad Razavi,2005 CMOS MOS

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