

# Digital Integrated Circuits A Design Perspective

Jan M. Rabaey Anantha Chandrakasan Borivoje Nikolic

# Semiconductor Memories

December 20, 2002

Digital Integrated Circuits<sup>2nd</sup>

Memories

# Chapter Overview

- Memory Classification
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# **Rabaey Digital Integrated Circuits Chapter 12**

Jan M. Rabaey, Anantha P. Chandrakasan, Borivoje Nikolić

#### Rabaey Digital Integrated Circuits Chapter 12

Digital Integrated Circuits Jan M. Rabaey, Anantha P. Chandrakasan, Borivoje Nikolić, 2003 Intended for use in undergraduate senior level digital circuit design courses with advanced material sufficient for graduate level courses Progressive in content and form this text successfully bridges the gap between the circuit perspective and system perspective of digital integrated circuit design Beginning with solid discussions on the operation of electronic devices and in depth analysis of the nucleus of digital design the text maintains a consistent logical flow of subject matter throughout The revision addresses today s most significant and compelling industry topics including the impact of interconnect design for low power issues in timing and clocking design methodologies and the tremendous effect of design automation on the digital design perspective The revision reflects the ongoing evolution in digital integrated circuit design especially with respect to the impact of moving into the deep submicron realm

**CMOS Digital Integrated Circuits** Sung-Mo Kang, Yusuf Leblebici, 2002 The fourth edition of CMOS Digital Integrated Circuits Analysis and Design continues the well established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design as well as addressing state of the art technology issues highlighted by the widespread use of nanometer scale CMOS technologies In this latest edition virtually all chapters have been re written the transistor model equations and device parameters have been revised to reflect the sigificant changes that must be taken into account for new technology generations and the material has been reinforced with up to date examples The broad ranging coverage of this textbook starts with the fundamentals of CMOS process technology and continues with MOS transistor models basic CMOS gates interconnect effects dynamic circuits memory circuits arithmetic building blocks clock and I O circuits low power design techniques design for manufacturability and design for testability

<u>Digital Integrated Circuits</u> Jan M. Rabaey,1996 Beginning with discussions on the operation of electronic devices and analysis of the nucleus of digital design the text addresses the impact of interconnect design for low power issues in timing and clocking design methodologies and the effect of design automation on the digital design perspective

**Analysis and Design of Analog Integrated Circuits** Paul R. Gray, Paul J. Hurst, Stephen H. Lewis, Robert G. Meyer, 2009-01-20 This is the only comprehensive book in the market for engineers that covers the design of CMOS and bipolar analog integrated circuits The fifth edition retains its completeness and updates the coverage of bipolar and CMOS circuits A thorough analysis of a new low voltage bipolar operational amplifier has been added to Chapters 6 7 9 and 11 Chapter 12 has been updated to include a fully differential folded cascode operational amplifier example With its streamlined and up to date coverage more engineers will turn to this resource to explore key concepts in the field

Introduction to VLSI Systems Ming-Bo Lin,2011-11-28 With the advance of semiconductors and ubiquitous computing the use of system on a chip SoC has become an essential technique to reduce product cost With this progress and continuous reduction of feature sizes and the development of very large scale integration VLSI circuits addressing the harder problems requires fundamental understanding of circuit and layout design issues Furthermore engineers can often develop their physical intuition to estimate the behavior of circuits rapidly without relying predominantly on computer aided design CAD tools Introduction to VLSI Systems A Logic Circuit and System Perspective addresses the need for teaching such a topic in terms of a logic circuit and system design perspective To achieve the above mentioned goals this classroom tested book focuses on Implementing a digital system as a full custom integrated circuit Switch logic design and useful paradigms that may apply to various static and dynamic logic families The fabrication and layout designs of complementary metal oxide semiconductor CMOS VLSI Important issues of modern CMOS processes including deep submicron devices circuit optimization interconnect modeling and optimization signal integrity power integrity clocking and timing power dissipation and electrostatic discharge ESD Introduction to VLSI Systems builds an understanding of integrated circuits from the bottom up paying much attention to logic circuit layout and system designs Armed with these tools readers can not only comprehensively understand the features and limitations of modern VLSI technologies but also have enough background to adapt to this ever changing field

The VLSI Handbook Wai-Kai Chen,2018-10-03 For the new millenium Wai Kai Chen introduced a monumental reference for the design analysis and prediction of VLSI circuits The VLSI Handbook Still a valuable tool for dealing with the most dynamic field in engineering this second edition includes 13 sections comprising nearly 100 chapters focused on the key concepts models and equations Written by a stellar international panel of expert contributors this handbook is a reliable comprehensive resource for real answers to practical problems It emphasizes fundamental theory underlying professional applications and also reflects key areas of industrial and research focus WHAT S IN THE SECOND EDITION Sections on Low power electronics and design VLSI signal processing Chapters on CMOS fabrication Content addressable memory Compound semiconductor RF circuits High speed circuit design principles SiGe HBT technology Bipolar junction transistor amplifiers Performance modeling and analysis using SystemC Design languages expanded from two chapters to twelve Testing of digital systems Structured for convenient navigation and loaded with practical solutions The VLSI Handbook Second Edition remains the first choice for answers to the problems and challenges faced daily in engineering practice

Digital Integrated Circuits,

**CMOS Digital Integrated Circuits** Sung-Mo Kang, Yusuf Leblebici, 1999 The second edition of this comprehensive text contains extensive revisions to reflect recent advances in technology and in circuit design practices Recognizing that the area of digital integrated circuit design is evolving at an increasingly fast pace every effort has been made to present state of the art material on all subjects covered in the book This book is primarily designed as a comprehensive text for senior level and first year graduate level digital circuit design classes as well as a reference for practicing engineers in the areas of IC design and VLSI

Negative Capacitance Field Effect Transistors Young Suh Song, Shubham Tayal, Shiromani Balmukund Rahi, Abhishek Kumar Upadhyay, 2023-10-31 This book aims to provide information in the ever growing field of low power electronic devices and their applications in portable devices wireless communication sensor and circuit domains Negative Capacitance Field Effect Transistors Physics Design Modeling and Applications discusses low power semiconductor technology and addresses state of the art techniques such as negative capacitance field effect transistors and tunnel field effect transistors The book is split into three parts The first part discusses the foundations of low power electronics including the challenges and demands and concepts such as subthreshold swing The second part discusses the basic operations of negative capacitance field effect transistors NCFETs and tunnel field effect transistors TFETs The third part covers industrial applications including cryogenics and biosensors with NC FET This book is designed to be a one stop guide for students and academic researchers to understand recent trends in the IT industry and semiconductor industry It will also be of interest to researchers in the field of nanodevices such as NC FET FinFET tunnel FET and device circuit codesign

Low-Voltage/Low-Power Integrated Circuits and Systems Edgar Sánchez-Sinencio, Andreas G. Andreou, 1999-01-13 Electrical Engineering Low Voltage Low Power Integrated Circuits and Systems Low Voltage Mixed Signal Circuits Leading experts in the field present this collection of original contributions as a practical approach to low power analog and digital circuit theory and design illustrated with important applications and examples Low Voltage Low Power Integrated Circuits and Systems features comprehensive coverage of the latest techniques for the design modeling and characterization of low power analog and digital circuits Low Voltage Low Power Integrated Circuits and Systems will help you improve your understanding of the trade offs between analog and digital circuits and systems It is an invaluable resource for enhancing your designs This book is intended for senior and graduate students It is also intended as a key reference for designers in the semiconductor and communication industries Highlighted applications include Low voltage analog filters Low power multiplierless YUV to RGB based on human vision perception Micropower systems for implantable defibrillators and pacemakers Neuromorphic systems Low power design in telecom circuits

The Dark Side of Silicon Amir M. Rahmani, Pasi Liljeberg, Ahmed Hemani, Axel Jantsch, Hannu Tenhunen, 2016-12-31 This book presents the state of the art of one of the main concerns with microprocessors today a phenomenon known as dark silicon Readers will learn how power constraints both leakage and dynamic power limit the extent to which large portions of a chip can be powered up at a given time i e how much actual performance and functionality the microprocessor can provide The authors describe their research toward the future of microprocessor development in the dark silicon era covering a variety of important aspects of dark silicon aware architectures including design management reliability and test Readers will benefit from specific recommendations for mitigating the dark silicon phenomenon including energy efficient dedicated solutions and technologies to maximize the utilization and reliability of microprocessors

**Digital Integrated Circuits** Evgeni Perelroyzen,2018-10-03 A current trend in digital design the integration of the MATLAB components Simulink and Stateflow for model building simulations system testing and fault detection allows for better control over the design flow process and ultimately for better system results Digital Integrated Circuits Design for Test Using Simulink and Stateflow illustrates the construction of Simulink models for digital project test benches in certain design for test fields The first two chapters of the book describe the major tools used for design for test The author explains the process of Simulink model building presents the main library blocks of Simulink and examines the development of finite state machine modeling using Stateflow diagrams Subsequent chapters provide examples of Simulink modeling and simulation for the latest design for test fields including combinational and sequential circuits controllability and observability deterministic algorithms digital circuit dynamics timing verification built in self test BIST architecture scan cell operations and functional and diagnostic testing The book also discusses the automatic test pattern generation ATPG process the logical determinant theory and joint test action group JTAG interface models Digital Integrated Circuits explores the possibilities of MATLAB s tools in the development of application specific integrated circuit ASIC design systems The book shows how to incorporate Simulink and Stateflow into the process of modern digital design

Thermal and Power Management of Integrated Circuits Arman Vassighi, Manoj Sachdev, 2006-06-01 In Thermal and Power Management of Integrated Circuits power and thermal management issues in integrated circuits during normal operating conditions and stress operating conditions are addressed Thermal management in VLSI circuits is becoming an integral part of the design test and manufacturing Proper thermal management is the key to achieve high performance quality and reliability Performance and reliability of integrated circuits are strong functions of the junction temperature A small increase in junction temperature may result in significant reduction in the device lifetime This book reviews the significance of the junction temperature as a reliability measure under nominal and burn in conditions The latest research in the area of electro thermal modeling of integrated circuits will also be presented Recent models and associated CAD tools are covered and various techniques at the circuit and system levels are reviewed Subsequently the authors provide an insight into the concept of thermal runaway and how it may best be avoided A section on low temperature operation of integrated circuits concludes the book

EBOOK: Fundamentals of Digital Logic Stephen Brown, Zvonko Vranesic, 2008-07-16 Fundamentals of Digital Logic with VHDL Design teaches the basic design techniques for logic circuits The text ptovides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism It emphasizes the synthesis of circuits and explains how circuits are implemented in real chips Fundamental concepts are illustrated by using small examples which are easy to understand Then a modular approach is used to show how larger circuits are designed VHDL is a complex language so it is introduced gradually in the book Each VHDL feature is presented as it becomes pertinent for the circuits being discussed While it includes a discussion of VHDL the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of VHDL and CAD tools A CD ROM containg all of the VHDL design examples used in the book as well Altera's Quartus II CAD software is included free with every text

Silicon Optoelectronic Integrated Circuits Horst Zimmermann, 2019-01-30 Explains the circuit design of silicon optoelectronic integrated circuits OEICs which are central to advances in wireless and wired telecommunications. The essential features of optical absorption are summarized as is the device physics of photodetectors and their integration in modern bipolar CMOS and BiCMOS technologies. This information provides the basis for understanding the underlying mechanisms of the OEICs described in the main part of the book. In order to cover the topic comprehensively Silicon Optoelectronic Integrated Circuits presents detailed descriptions of many OEICs for a wide variety of applications from various optical sensors smart sensors 3D cameras and optical storage systems DVD to fiber receivers in deep sub m CMOS Numerous detailed illustrations help to elucidate the material

<u>Digital Integrated Circuit Design</u> Hubert Kaeslin,2008-04-28 This practical tool independent guide to designing digital circuits takes a unique top down approach reflecting the nature of the design process in industry Starting with architecture design the book comprehensively explains the why and how of digital circuit design using the physics designers need to know and no more

**The Physics of Semiconductor Devices** R. K. Sharma, D.S. Rawal, 2019-01-31 This book disseminates the current knowledge of semiconductor physics and its applications across the scientific community It is based on a biennial workshop that provides the participating research groups with a stimulating platform for interaction and collaboration with colleagues from the same scientific community The book discusses the latest developments in the field of III nitrides materials devices compound semiconductors VLSI technology optoelectronics sensors photovoltaics crystal growth epitaxy and characterization graphene and other 2D materials and organic semiconductors

Minimizing and Exploiting Leakage in VLSI Design Nikhil Jayakumar, Suganth Paul, Rajesh Garg, 2009-12-02 Power consumption of VLSI Very Large Scale Integrated circuits has been growing at an alarmingly rapid rate This increase in power consumption coupled with the increasing demand for portable hand held electronics has made power consumption a dominant concern in the design of VLSI circuits today Traditionally dynamic switching power has dominated the total power consumption of an IC However due to current scaling trends leakage power has now become a major component of the total power consumption in VLSI circuits Leakage power reduction is especially important in portable hand held electronics such as cell phones and PDAs This book presents two techniques aimed at reducing leakage power in digital VLSI ICs The first technique reduces leakage through the selective use of high threshold voltage sleep transistors The second technique reduces leakage by applying the optimal Reverse Body Bias RBB voltage This book also shows readers how to turn the leakage problem into an opportunity through the use of sub threshold logic

**CMOS Logic Circuit Design** John P. Uyemura,2007-05-08 This is an up to date treatment of the analysis and design of CMOS integrated digital logic circuits The self contained book covers all of the important digital circuit design styles found in modern CMOS chips emphasizing solving design problems using the various logic styles available in CMOS

Fast Hopping Frequency Generation in Digital CMOS Mohammad Farazian, Lawrence E. Larson, Prasad S. Gudem, 2012-10-12 Overcoming the agility limitations of conventional frequency synthesizers in multi band OFDM ultra wideband is a key research goal in digital technology This volume outlines a frequency plan that can generate all the required frequencies from a single fixed frequency able to implement center frequencies with no more than two levels of SSB mixing It recognizes the need for future synthesizers to bypass on chip inductors and operate at low voltages to enable the increased integration and efficiency of networked appliances The author examines in depth the architecture of the dividers that generate the necessary frequencies from a single base frequency and are capable of establishing a fractional division ratio Presenting the first CMOS inductorless single PLL 14 band frequency synthesizer for MB OFDMUWB makes this volume a key addition to the literature and with the synthesizer capable of arbitrary band hopping in less than two nanoseconds it operates well within the desired range on a 1 2 volt power supply The author's close analysis of the operation stability and phase noise of injection locked regenerative frequency dividers will provide researchers and technicians with much food for developmental thought

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